

## IN THE SPECIFICATION

Please replace the paragraph on page 5, beginning at line 6, with the following paragraph:

When the combination of the control signals inputted currently represents, for example, a command input, the signals CMD[i] inputted to input/output pins IOi are passed to the command register block 120. ~~And, w~~When the combination of the control signals inputted currently represents, for example, an address input, then the signals XAD[i], ~~X~~ and YAD[i] outputted from the global buffer and control buffer block 110 are passed to the row address buffer and decoder block 130 and the column address buffer and decoder block 140, respectively. When the combination of the control signals inputted currently represents, for example, a data input, the signal Data[i] outputted from the global buffer and control buffer block 110 according to the signals inputted to input/output pins IOi are passed to the data input/output control block 180.